



32nd IEEE International Parallel and Distributed Processing Symposium

May 21-25, 2018
Vancouver, British Columbia
CANADA

ANNOUNCING 23 PLANNED WORKSHOPS

IPDPS Workshops Monday 21 May 2018

HCW	Heterogeneity in Computing Workshop
RAW	Reconfigurable Architectures Workshop
HiCOMB	High Performance Computational Biology
GABB	Graph Algorithms Building Blocks
EduPar	NSF/TCPP W. on Parallel and Distributed Computing Education
HIPS	High Level Programming Models and Supportive Environments
HPBDC	High-Performance Big Data, Deep Learning, and Cloud Computing
AsHES	Accelerators and Hybrid Exascale Systems
PDCO	Parallel / Distributed Computing and Optimization
HPPAC	High-Performance, Power-Aware Computing
APDCM	Advances in Parallel and Distributed Computational Models
ParLearning	Parallel and Distributed Computing for Large-Scale Machine Learning and Big Data Analytics

IPDPS Workshops Friday 25 May 2018

CHIWW	Chapel Implementers and Users Workshop
PDSEC	Parallel and Distributed Scientific and Engineering Computing
JSSPP	Job Scheduling Strategies for Parallel Processing
iWAPT	International Workshop on Automatic Performance Tunings
ParSocial	Parallel and Distributed Processing for Computational Social Systems
GraML	Graph Algorithms and Machine Learning
CEBDA	Convergence of Extreme Scale Computing and Big Data Analysis
MPP	Parallel Programming Model: Special Edition on Edge/Fog/In-Situ Computing
PASCO	Parallel Symbolic Computation
PMAW	Programming Models and Algorithms Workshop
ROME	Runtime and Operating Systems for the Many-core Era

IPDPS Workshops are the “bookends” to the three-day conference technical program of contributed papers, invited speakers, student programs, and industry participation. They provide the IPDPS community an opportunity to explore special topics and present work that is more preliminary or cutting-edge than the more mature research presented in the main symposium.

Each workshop has its own website and submission requirements, and the **submission deadline for most workshops is after the main conference author notification dates**. When a workshop announces its Call for Papers, the link on the IPDPS Workshops webpage is activated, and the call for papers submission due date is posted. Proceedings of the workshops are distributed at the conference and are submitted for inclusion in the IEEE Xplore Digital Library after the conference.

GENERAL CHAIR

Bora Uçar (CNRS and ENS Lyon, France)

PROGRAM CHAIR and VICE-CHAIR

Anne Benoit (ENS Lyon, France) and
Ümit V. Çatalyürek (Georgia Institute of Technology, USA)

WORKSHOPS CHAIR and VICE-CHAIR

Erik Saule (University of North Carolina at Charlotte, USA) and
Jaroslav Zola (University at Buffalo, USA)

STUDENT PARTICIPATION CHAIR and VICE-CHAIR

Trilce Estrada (University of New Mexico, USA) and
Jay Lofstead (Sandia National Laboratories, USA)

PHD FORUM & STUDENT MENTORING

This event will include traditional poster presentations by PhD students enhanced by a program of mentoring and coaching in scientific writing and presentation skills and a special opportunity for students to hear from and interact with senior researchers attending the conference.

INDUSTRY PARTICIPATION

IPDPS extends a special invitation for companies to become an IPDPS 2018 Industry Partner and to share in the benefits of associating with an international community of top researchers and practitioners in fields related to parallel processing and distributed computing. Visit the IPDPS website to see ways to participate.

IPDPS 2018 VENUE



Rising against a backdrop of majestic coastal mountains on the Pacific Northwest coast, the JW Marriott Parq Vancouver is located in the heart of downtown Vancouver's urban entertainment and resort complex. IPDPS 2018 attendees will enjoy state of the art meeting facilities, with Vancouver as a jumping off point for some of the world's grand sightseeing adventures.

For details, visit www.ipdps.org

IMPORTANT DATES

- Conference Preliminary Author Notification **December 8, 2017**
- Workshops' Call for Papers Deadlines **Most Fall After December 8, 2017**

IEEE  computer society
Sponsored by IEEE Computer Society
Technical Committee on Parallel Processing

 
In cooperation with
ACM SIGARCH & SIGHPC and IEEE TCCA & TCDP