IPDPS 2015PhD Forum

Research Projects Selected for Poster Presentation

1	In-memory Distance Threshold Searches On Moving Object Trajectories Michael Gowanlock, University of Hawaii at Manoa
2	Performance Improvement of MapReduce Parallel Processing Framework in Heterogeneous Environments Nenavath Srinivas Naik, University of Hyderabad
3	Investigation on Design Issues and Analysis of Parallel Scalable High Performance Publish Subscribe Systems Medha Shah, Walchand College of Engineering
4	A Model-driven Approach for Energy-time Performance of Heterogeneous Systems Lavanya Ramapantulu, National University of Singapore
5	Resilience in High Level Parallel Programming Languages Sara S Hamouda, Australian National University
6	Improving the Cache Energy-Efficiency in Emerging Mobile Platforms Kaige Yan, University of Houston
7	Exploring Soft-Error Robust and Energy-Efficient Register File in GPGPUs using STT-RAM Jingweijia Tan, University of Houston
8	Data Analysis from Distributed File Systems on Mobile Platforms using Map Reduce Madhavi Vaidya, University of Mumbai & VES College of Arts, Science and Commerce
9	Design of Efficient Secure Data Communication Techniques for Reconfigurable Hardware Platform Suman Sau, Calcutta University
10	Parallel Graph Algorithms on Modern Systems George Slota, The Pennsylvania State University
11	Energy-efficiency and Cache-adaptivity of Recursive Divide and Conquer Algorithms Jesmin Jahan Tithi, Stony Brook University
12	Improved Scheduler for Multi-core Many-core Systems Neetesh Kumar, Jawaharlal Nehru University
13	Hot and Cold Data Classification for Main Memory Databases Trupti Padiya, DA-IICT
14	Using Multi-key Quicksort for String Sorting on GPU: A Rework Anjjan Narayan, NMAM Institute of Technology
15	String sorting on the GPU Crystal Gomes, NMAM Institution Of Technology
16	Evaluating Memory Management Schemes in CPU-GPU Architectures Shilpa Babalad, Indian Institute of Science
17	Dynamic Job Scheduling and Resource Management for High Performance Computing Suraj Prabhakaran, Technische Universität Darmstadt
18	Toward Effective Speculative Checkpointing for HPC Applications Muhammad Alfian Amrizal, Tohoku University
19	Exposing and Exploiting Parallelism in Sparse LU Factorization for FPGA Accelerators Siddhartha, Nanyang Technological University