

29th IPDPS 2015 WORKSHOPS

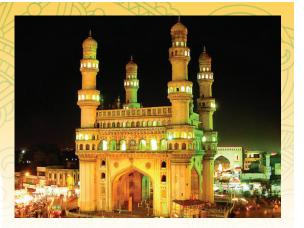
IPDPS Workshops, held on the first and last day of the conference, are a major part of the IPDPS week-long compendium of events which include the papers selected from October submissions for the conference. The twenty-two workshops listed below will provide the IPDPS community an opportunity to explore special topics and/or present work that is more preliminary or cutting-edge than the more mature research presented in the main symposium. Proceedings of the workshops are published by the IEEE Digital Library and distributed at the conference. Each workshop has its own requirements and schedule for submissions, and all are linked from the IPDPS Website.

Note that the <u>submission deadline for most workshops is after</u> the main symposium author notification date of December12, 2014.

| RAW Reconfigurable Architectures Workshop HIPS High-Level Parallel Programming Models & Supportive Environments NIDISC Nature Inspired Distributed Computing HiCOMB High Performance Computational Biology | |
|--|------|
| NIDISC Nature Inspired Distributed Computing | |
| | |
| HiCOMB High Performance Computational Biology | |
| | |
| APDCM Advances in Parallel and Distributed Computing Models | |
| HPPAC High-Performance, Power-Aware Computing | |
| HPBC High Performance Big Data and Cloud Computing | |
| ASHES Accelerators and Hybrid Exascale Systems | |
| PLC Programming Models, Languages and Compilers for Manycore and Heterogeneous Architectu | ires |
| EduPar NSF/TCPP Workshop on Parallel and Distributed Computing Education | |
| GABB Graph Algorithms Building Blocks | |
| PDSEC Parallel and Distributed Scientific and Engineering Computing | |
| DPDNS Dependable Parallel, Distributed and Network-Centric Systems | |
| LSPP Large-Scale Parallel Processing | |
| PCO Parallel Computing and Optimization | |
| Par Parallel and Distributed Computing for Machine Learning and Inference Problems | |
| HPDIC High Performance Data Intensive Computing | |
| JSSPP Job Scheduling Strategies for Parallel Processing | |
| HiPeR High-Performance Runtime | |
| HPDAV High Performance Data Analysis and Visualization | |
| iWAPT International Workshop on Automatic Performance Tuning | |

IEEE Computer society

IEEE Hyderabad com



IEEE IPDPS 2015 IN HYDERABAD, INDIA

The Hyderabad International Convention Centre will host an event that offers the full IPDPS program of workshops, contributed papers, and keynote speakers as well as representatives from industry and opportunities for students to hear from and interact with senior researchers attending the conference. The Hyderabad airport (RGIA) has direct flights from all international hubs, and Hyderabad offers a variety of tourist attractions for attendees and their families. Visit the IPDPS Website regularly to see program updates and to get tips on travel to the premier conference in parallel and distributed computing. Go to www.ipdps.org.

General Co-Chairs

Susamma Barua (California State University, Fullerton, USA) R. Govindarajan (Indian Institute of Science, Bangalore, India)

Program Chair Srinivas Aluru (Georgia Institute of Technology, USA)

Workshops Chair Ümit V. Çatalyürek (Ohio State University, USA)

Workshops Vice Chair Bora Uçar (CNRS and ENS Lyon, France)

The IPDPS PhD Forum event will continue with the traditional poster presentations by students working toward a PhD and will offer an enhanced student program to

provide coaching in scientific writing and presentation skills and to give students an opportunity to hear from and interact with senior researchers and industry partners. **IPDPS extends a special invitation to companies both in and outside India to consider becoming IPDPS 2015 Industry Partners**,

sharing in the benefits of associating with an international community of top researchers and practitioners in fields related to parallel processing and distributed computing.





Sponsored by IEEE Computer Society • Technical Co-sponsor: IEEE Hyderabad Section • Hosts: IISc & IIIT, Hyderabad In cooperation with ACM SIGARCH, IEEE CS Technical Committees on Computer Architecture & Distributed Processing